Micro project

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| **Feature** | **Working or not** | **known bugs** | **Copied or not** |
| screen to choose the initial points and name | Working | None | not copied |
| comparison between initial points | Working | None | not copied |
| level selection screen | Working | None | not copied |
| forbidden char screen | Working | None | not copied |
| the char appears on both screens | Working | None | not copied |
| the game screen with reg. initialized by zero | Working | None | not copied |
| the reg. are changed after commands | Working | None | not copied |
| power ups: executing a command on your own processor power up | Working | None | not copied |
| changing the forbidden char power up | Working | None | not copied |
| clearing all reg.at once power up | Working | None | not copied |
| when command invalid minus 1 point | Working | None | not copied |
| invalid reg. name | Working | None | not copied |
| flying objects and the gun | Working | None | not copied |
| forbidden char hidden at level 2 | Working | None | not copied |
| user able to decide whether to execute on his reg. or his opponent at level 2 | Working | None | not copied |
| put initial values to the reg. at level 2 | Working | None | not copied |
| if any player's points reach zero s/he loses. | Not Working | None | not copied |
| MOV with register addressing mode | Working | None | not copied |
| MOV with immediate addressing mode | Working | None | not copied |
| MOV with direct addressing mode | Working | None | not copied |
| MOV with indirect register addressing mode | Working | None | not copied |
| Detecting size mismatch | Working | None | not copied |
| ADD with register addressing mode | Working | None | not copied |
| ADD with immediate addressing mode | Working | None | not copied |
| ADD with direct addressing mode | Working | None | not copied |
| ADD with indirect register addressing mode | Working | None | not copied |
| ADC with register addressing mode | Working | None | not copied |
| ADC with immediate addressing mode | Working | None | not copied |
| ADC with direct addressing mode | Working | None | not copied |
| ADC with indirect register addressing mode | Working | None | not copied |
| SUB with register addressing mode | Working | None | not copied |
| SUB with immediate addressing mode | Working | None | not copied |
| SUB with direct addressing mode | Working | None | not copied |
| SUB with indirect register addressing mode | Working | None | not copied |
| SUBB with register addressing mode | Working | None | not copied |
| SUBB with immediate addressing mode | Working | None | not copied |
| SUBB with direct addressing mode | Working | None | not copied |
| SUBB with indirect register addressing mode | Working | None | not copied |
| XOR with register addressing mode | Working | None | not copied |
| XOR with immediate addressing mode | Working | None | not copied |
| XOR with direct addressing mode | Working | None | not copied |
| XOR with indirect register addressing mode | Working | None | not copied |
| AND with register addressing mode | Working | None | not copied |
| AND with immediate addressing mode | Working | None | not copied |
| AND with direct addressing mode | Working | None | not copied |
| AND with indirect register addressing mode | Working | None | not copied |
| OR with register addressing mode | Working | None | not copied |
| OR with immediate addressing mode | Working | None | not copied |
| OR with direct addressing mode | Working | None | not copied |
| OR with indirect register addressing mode | Working | None | not copied |
| NOP with register addressing mode | Working | None | not copied |
| NOP with immediate addressing mode | Working | None | not copied |
| NOP with direct addressing mode | Working | None | not copied |
| NOP with indirect register addressing mode | Working | None | not copied |
| SHR | Working | None | not copied |
| SHL | Working | None | not copied |
| CLC with register addressing mode | Working | None | not copied |
| CLC with immediate addressing mode | Working | None | not copied |
| CLC with direct addressing mode | Working | None | not copied |
| CLC with indirect register addressing mode | Working | None | not copied |
| ROR | Working | None | not copied |
| RCL | Working | None | not copied |
| RCR | Working | None | not copied |
| ROL | Working | None | not copied |
| INC with register addressing mode | Working | None | not copied |
| INC with immediate addressing mode | Working | None | not copied |
| INC with direct addressing mode | Working | None | not copied |
| INC with indirect register addressing mode | Working | None | not copied |
| DEC with register addressing mode | Working | None | not copied |
| DEC with immediate addressing mode | Working | None | not copied |
| DEC with direct addressing mode | Working | None | not copied |
| DEC with indirect register addressing mode | Working | None | not copied |